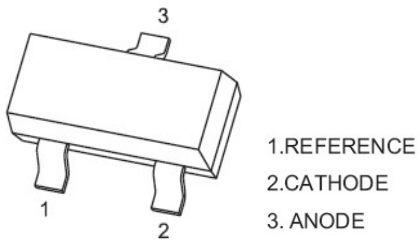
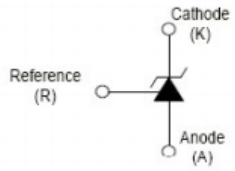
SOT-23



Equivalent Circuit



MARKING: 431

Device Description

The TL431BQDBZR-CN is a three-terminal adjustable shunt regulator offering excellent temperature stability. This device has a typical dynamic output impedance of 0.2Ω. The device can be used as a replacement for zener diodes in many applications.

Features

- The output voltage can be adjusted to 36V
- Low dynamic output impedance, its typical value is 0.2Ω
- Trapping current capability is 1 to 100mA
- Low output noise voltage
- Fast on-state response
- The effective temperature compensation in the working range of full temperature
- The typical value of the equivalent temperature factor in the whole temperature scope is 50 ppm/℃

Application

- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter

Mechanical Data

- 封装: SOT-23 封装 SOT-23 Small Outline Plastic Package.
- 环氧树脂 UL 易燃等级 Epoxy UL: 94V-0.
- 安装位置: 任意 Mounting Position: Any.

极限值和温度特性(TA = 25℃ 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25℃ ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Cathode Voltage	V_{KA}	36	V
Cathode Current Range(Continuous)	I_{KA}	-100~+150	mA
Reference Input Current Range	I_{ref}	0.05~+10	mA
Power Dissipation	P_D	300	mW
Junction Temperature	T_j	150	℃
Operating Temperature	Topr	-25-+85	℃
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	417	℃/W

电特性 (TA = 25℃ 除非另有规定)

Electrical Characteristics (Ratings at 25℃ ambient temperature unless otherwise specified).

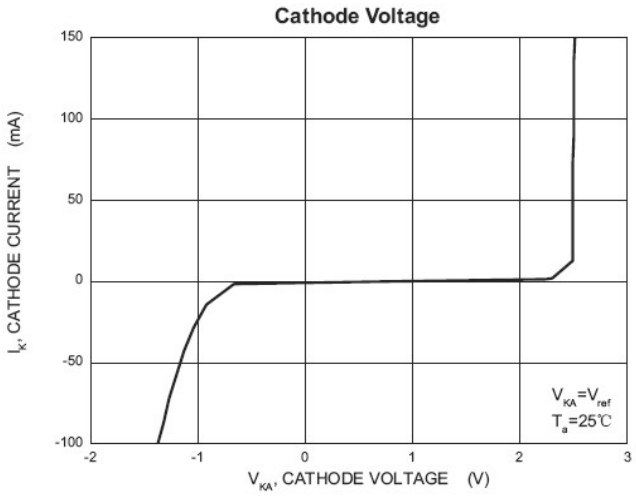
参数 Parameter	符号 Symbols	测试条件 Test Condition		界限 Limits Min	Typ	Max	单位 Unit
Reference input Voltage	**Vref**	V_{KA}= V_{REF} V, I_{KA}=10mA		2.475	2,5	2.525	V
Deviation of reference input voltage over temperature(note	**△Vref/△T**	V_{KA}= V_{REF}, I_{KA}=10mA T_{MIN}≤Ta≤T_{MAX}			4.5	17	mV
Ratio of change in reference Input voltage to the change in cathode voltage	**△Vref/△VKA**	I_{KA}=10mA	**△VKA=10V**~V_{REF}		-1.0	-2.7	mV/v
			△VKA=36V~10V		-0.5	-2.0	mV/v
Reference input current	**Iref**	I_{KA}=10mA, R_1=10KΩ, R2=∞			1.5	4	uA
Deviation of reference input current over full temperature	**△Iref/△T**	I_{KA}=10mA, R_1=10KΩ, R2=∞ TA=-25 to 85℃			0.4	1.2	uA
Minimum cathode current for regulation	**$I_{KA(min)}$**	V_{KA}=V_{REF}			0.45	1.0	mA
Off-state cathode current	**$I_{KA(off)}$**	V_{KA}=36V, V_{REF}=0			0.05	1.0	uA
Dynamic impedance	**Z_{KA}**	VKA=VREF, IKA=1 to 100mA, f≤1.0kHz			0.15	0.5	Ω

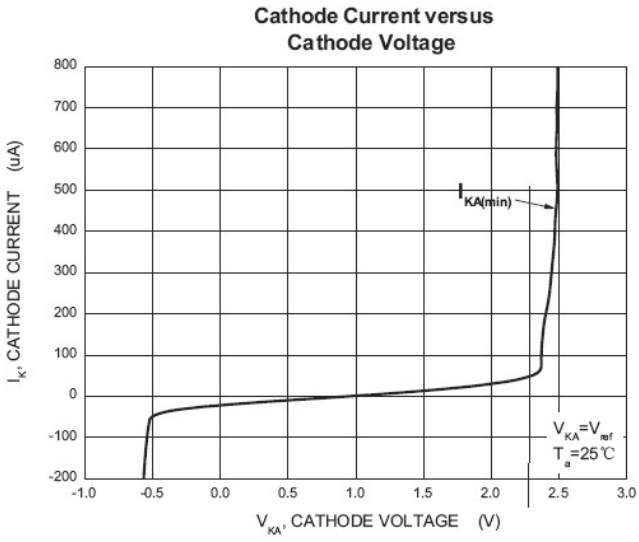
Note: T_{MIN}=-25℃, T_{MAX}=+85℃.

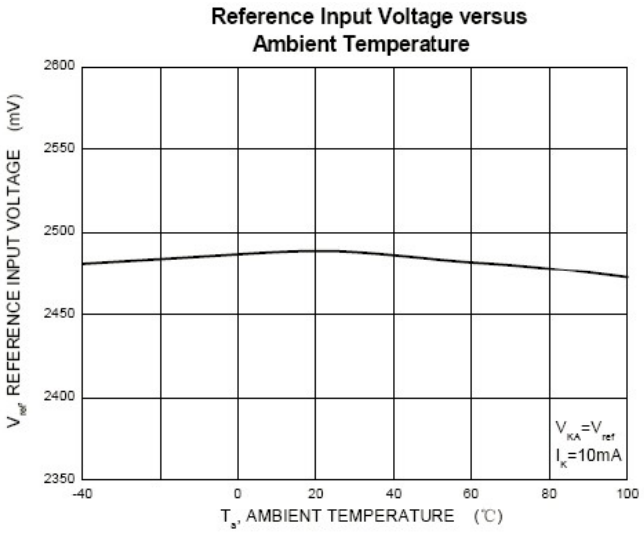
CLASSIFICATION of Vref

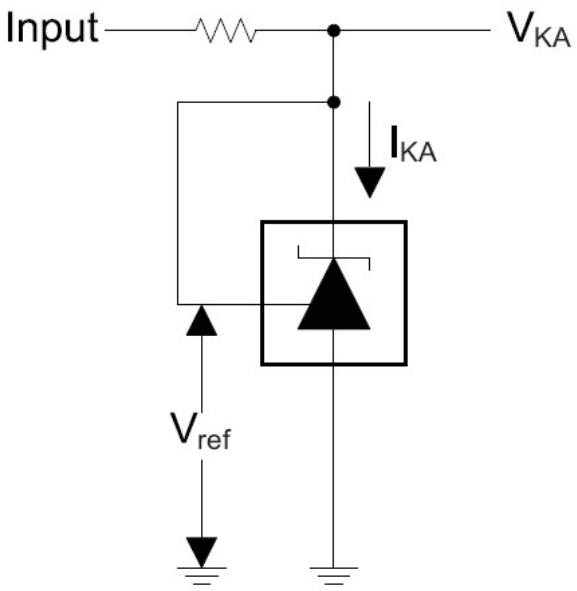
Rank	0.5%	1%
Rank	2.487-2.513	2.475-2.525

Typical characteristics

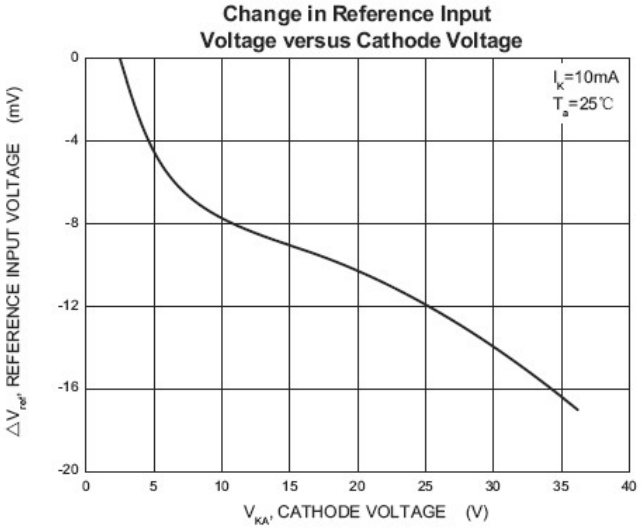


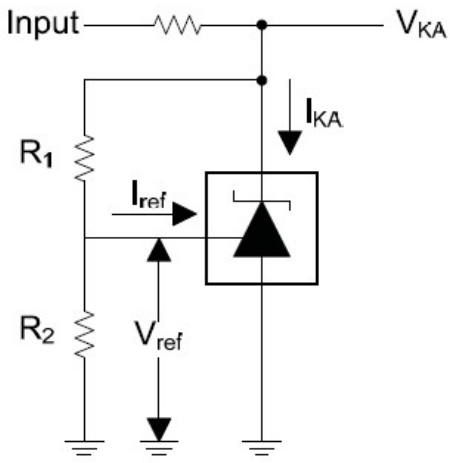




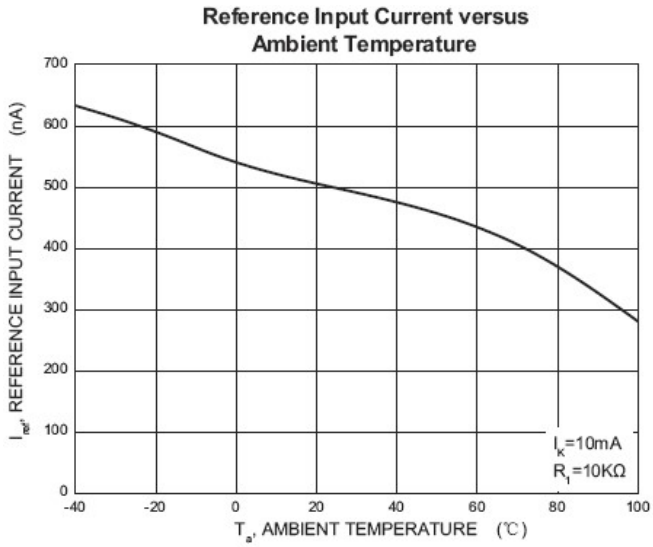


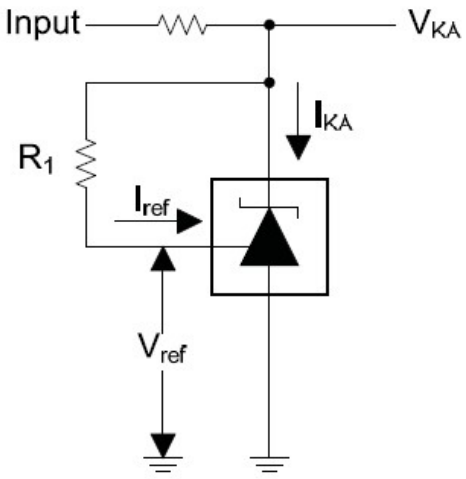
Test Circuit for $V_{KA}=V_{ref}$



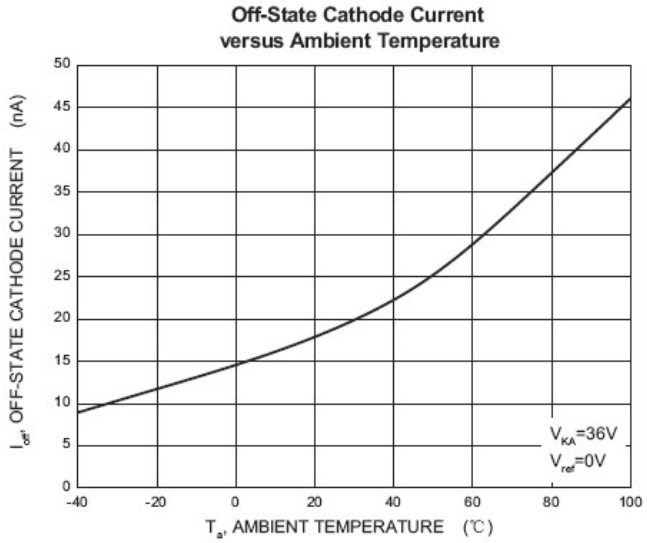


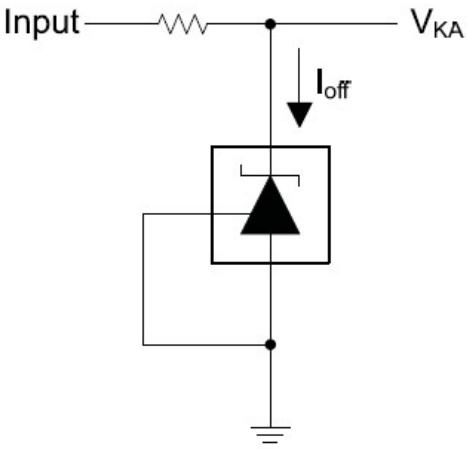
Test Circuit for $V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}$





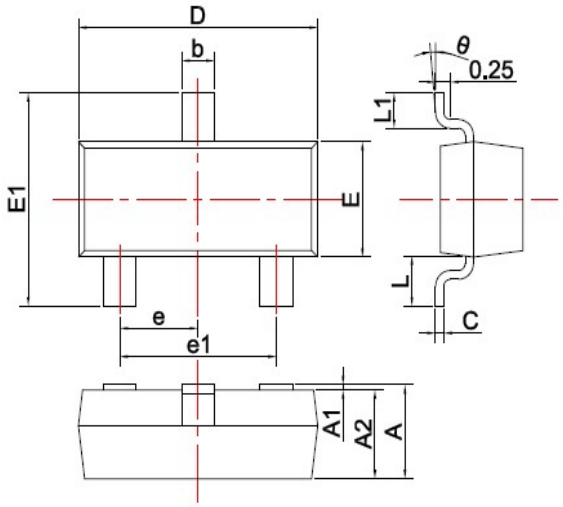
Test Circuit for I_{ref}





Test Circuit for I_{off}

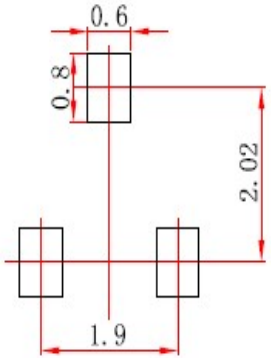
SOT-23 PACKAGE OUTLINE Plastic surface mounted package



SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

Unit: mm

焊盘设计参考Precautions: PCB Design
Recommended land dimensions for SOT-23 diode. Electrode patterns for PCBs



Note:
1.Controlling dimension:In millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

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